

Troubleshooting list:

The following troubleshooting list should help you to clarify the most detected errors and give you a helping hand how to avoid such faulty behaviors. Also the current versions of the existing FW-stacks are included. These versions should be double checked by the user in advance.

Issue	Problem	Solution
SPC3	During operation with S7 the slave reacts in a strange way (normally it doesn't react anymore)	The SAP-List Pointer at address 0x17 doesn't point to a RAM cell inside of the SPC3 where 0xff is stored.
SPC3	Sporadically read or write cycles to the SPC3 doesn't work properly.	Maybe this collides with the specified timing 37 or 79. It's also important to check the signal form. Even small spikes can lead to sporadic problems.
SPC3	Although the parameterization telegram and the configuration telegram are acknowledged the SPC3 doesn't branch to Data Exchange.	After having acknowledged these both telegrams the user has to take care that the first input-data have to be stored in the SPC3 (Input-Update)
SPC3	During start up a GC-Unclear is not indicated to the user.	GC will only be indicated when the received GC-Command distinguishes from the value stored in R_GC_Command. If the user always wants to get an indication then the cell R_GC_Command must always be overwritten with 0xff.
SPC3	The slave doesn't branch to data exchange or the slaves branches to a new start up during data exchange while the master is in clear mode.	Maybe the GSD-file says Fail_Safe =1 and in the Mode Register the Bit SPEC_CLEAR_MODE is set to 0.
DPS2	-----	Current FW V1.30
DPSE	#define DPSE_DP is not seperately possible	Error in V1.21
DPSE	The service Set_slave_addr is wrong	Error in V1.21
DPSE	Function DPSE_WRITE_REQ() with return value DPSE_DELAY is wrong.	Error in V1.22
DPSE	Intel/Motorola-format during INITIATE	Error in V1.22
DPSE	Interrupt also enabled during polling	Error in V1.22
DPSE	-----	Current FW V1.23
DPC31	pbk_open_device sometimes returns the error PBC_INITF_LESS_MEM	Error in V1.0: The structure element dpc31.com_user_ram_segments is not initialized in the existing example. Work around: Initialisation in sys_main.c with DPC31_USER_RAM_SEGMENTS
DPC31	Slave is operating correct with S7 400. If the cyclic transmission of the bus parameters is switched on, the slave fails.	Error in V1.0: Please check that the Temp-Buffer is fixed to 256 instead of 32.
V1SL	pbk_open_device function call is faulty	Error in V1.2 Please correct function call in SYS_MAIN.C .. Unsigned handle;V1SL_OK != pbk_open..(&...&handle)..
V1SL	16 bit values of the inputs and acyclic	Please exchange the 16 Bit values when

	data are exchanged when using an Intel-Mode Controller	using the example program in the DP/PA-Kit in combination with the CP5613-program
V1SL	The use of variables inside the memory of the DPC31 causes wrong reactions	In the structure V1SL_STRUC_PBC_DETAIL the variable com_user_ram_segments has to be set to DPC31_USER_RAM_SEGMENTS - x. x = number of segments which are needed to store the variables. Afterwards store data at the correct address.
V1SL	-----	Current FW V2.00